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DATE MAILED: 09/03/2004

PPLICATION NO.	FILING DAT	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO		
10/648,206	08/27/2003	Alfred P. Turley	1215-0485P (000277-078)	3492		
2292	7590 09/0	/2004	EXAM	EXAMINER		
	WART KOLAS	LOKE, STEV	LOKE, STEVEN HO YIN			
PO BOX 747 FALLS CHU	RCH, VA 2204	ART UNIT	PAPER NUMBER			
			2811			

Please find below and/or attached an Office communication concerning this application or proceeding.

		App	lication No.	Applicant(s)				
Office Action Summan		10/6	48,206	TURLEY, ALFREI	TURLEY, ALFRED P.			
Office Action Summary			niner	Art Unit				
			en Loke	2811				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply								
THE   - External after - If the - If NO - Failu Any	ORTENED STATUTORY PERIOD FOR MAILING DATE OF THIS COMMUNICA masions of time may be available under the provisions of 3i SIX (6) MONTHS from the mailing date of this communic period for reply specified above is less than thirty (30) de period for reply is specified above, the maximum statuto re to reply within the set or extended period for reply will, reply received by the Office later than three months after the patent term adjustment. See 37 CFR 1.704(b).	TION. 7 CFR 1.136(a). Institution. ays, a reply within try period will apply by statute, cause to	no event, however, may a reply be ti he statutory minimum of thirty (30) da and will expire SIX (6) MONTHS fron he application to become ABANDONI	mely filed ys will be considered timel the mailing date of this c ED (35 U.S.C. § 133).				
Status								
1)	Responsive to communication(s) filed of	on						
2a)□	This action is FINAL. 2b)⊠ This action is non-final.							
3)□	3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is							
	closed in accordance with the practice	under <i>Ex par</i>	te Quayle, 1935 C.D. 11, 4	53 O.G. 213.				
Dispositi	on of Claims							
5) <u> </u>	Claim(s) 1-12 is/are pending in the application.  4a) Of the above claim(s) is/are withdrawn from consideration.  Claim(s) is/are allowed.  Claim(s) is/are rejected.							
·	7) ☐ Claim(s) is/are objected to. 8) ☑ Claim(s) <u>1-12</u> are subject to restriction and/or election requirement.							
Applicati	ion Papers							
9) The specification is objected to by the Examiner.								
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.								
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.								
Priority (	ınder 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  a) All b) Some * c) None of:  1. Certified copies of the priority documents have been received.  2. Certified copies of the priority documents have been received in Application No  3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  * See the attached detailed Office action for a list of the certified copies not received.								
Attachmen	t(s)							
	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-		) Interview Summary (PTO-413) Paper No(s)/Mail Date					
3) 🔲 Infor	mation Disclosure Statement(s) (PTO-1449 or PT0 r No(s)/Mail Date		5) Notice of Informal 6) Other:	Patent Application (PT	O-152)			

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1. Restriction to one of the following inventions is required under 35 U.S.C. 121:

- Claims 1-6, drawn to a semiconductor device, classified in class 257, subclass 552.
- II. Claims 7-12, drawn to a method to make a semiconductor device, classified in class 438, subclass 22+.

The inventions are distinct, each from the other because of the following reasons:

- 2. Inventions II and I are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case, in claim 7, forming a semiconductor layer of the bipolar transistor over the area occupied by the CMOS transistor, then etching a portion of the semiconductor layer over the area occupied by the CMOS transistor.
- 3. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.
- 4. Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven Loke whose telephone number is (571) 272-1657. The examiner can normally be reached on 7:50 am to 5:20 pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Eddie Lee can be reached on (571) 272-1732. The fax phone number for

the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

September 1, 2004

Primary Examiner

Steven Loke

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